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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:)
Shinji TAKEDA et al.) Atty. Docket No.: **TM&K0004**
Serial No. 09/543,247)
Filed: April 5, 2000) Group Art Unit: 2811
For: SEMICONDUCTOR DEVICE AND)
PROCESS FOR FABRICATION) Examiner: D. GRAYBILL
THEREOF) Date: April 20, 2001

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D. C. 20231

Sir:

This is an Information Disclosure Statement with regard to the above-identified application. Form PTO/SB/08A is attached.

Copies of the cited documents can be found in the applicant's parent U. S. Patent Application Serial No. 08/981,702, and the relevant of these documents is discussed therein. Accordingly, no further comment in regard to the disclosures of these documents is believed to be required.

Additionally, the Examiner is advised that the following are related to the above-captioned application: U. S. Patent No. 6,099,678 and U. S. Patent Applications Serial Nos. 09/616,943, 09/785,194, 09/785,436 and 09/785,486.

It is respectfully requested that the attached documents be considered and officially

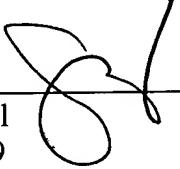
cited, and that a copy of the Form PTO/SB/08A, initialed by the Examiner to indicate that the documents have been considered, be returned to the applicants.

It is believed that the present Information Disclosure Statement complies with the requirements of 37 C.F.R. §§ 1.97-8, but if a fee should be required, authorization is hereby given to deduct from deposit account No. 501281.

Respectfully submitted,

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